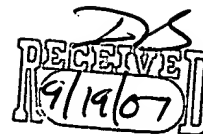


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TO 11671#3058500016 P.02/03

PATENT

DOCKET NO. 30585/16

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): John S. Yates, Jr., et al.

Serial No.: 09/429,094

Filed: October 28, 1999

Title: SIDE TABLES ANNOTATING AN INSTRUCTION STREAM

Art Unit: 2783

Examiner: D. Eng

COMMISSIONER FOR PATENTS

Washington D.C. 20231

CLEAN COPY OF CLAIMS AS AMENDED AUGUST 21, 2001

After entering the Supplemental Amendment of August 21, 2001, claims 11 and 25 stand

as follows:

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11. The microprocessor chip of claim 10, further comprising:
a binary translator programmed to translate at least a selected portion of a computer program from a first binary representation to a second binary representation; and
wherein the pipeline control circuitry is further designed to initiate a determination of whether to transfer control from an execution of the instruction, the instruction being an instruction of the first binary representation of the program, to the second binary representation, and effective to initiate the determination with neither a query nor a transfer of control to the second binary representation being coded into the first binary representation.

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25. A method, comprising the steps of:
as an integral part of processing an instruction in instruction pipeline circuitry of a computer, consulting a lookup structure of entries, each entry corresponding to an address range translated by address translation circuitry, and describing a likelihood of the existence of an alternate coding of instructions located in the respective corresponding address range; and
as a result of the consulting, changing an instruction set architecture under which instructions are interpreted by the computer.

I certify that this correspondence, along with any documents referred to therein, is being transmitted by FAX on September 19, 2001 to The Commissioner for Patents, Washington D.C. 20231.

Clean Copy of Claims as Amended August 21, 2001
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